

**FORM PTO-1449 (MODIFIED)****LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT**In re Application of:  
Siamack Nemazie et al.Docket No.: 0924-MS-L-DIV  
RPB REF: CRUS-0219

Serial Number: UNASSIGNED

Art Unit: UNASSIGNED

Filing Date: HEREWITH

Examiner: UNASSIGNED

Title: MIXED-SIGNAL SINGLE-CHIP INTEGRATED SYSTEM ELECTRONICS FOR DATA STORAGE DEVICES

**OTHER ART (including Author, Title, Pertinent Pages, etc.)**

RP	AC	Welland, D. R., et. al., <i>A Digital Read/Write Channel with EPR4 Detection</i> , (ISSCC Digest of Technical Papers, pp.276277, Feb., 1994)
	AD	Mita, S. et. al., <i>A 150 Mb/s PRML Chip for Magnetic Disk Drives</i> , (ISSCC Digest of Technical Papers, pp.62-63, Feb., 1996)
	AE	Fields, J., et. al., <i>A 200 Mb/s EPRML Channel with Integrated Servo Demodulator for Magnetic Disks</i> , (ISSCC Digest of Technical Papers, pp.314-315, Feb., 1997)
	AF	Tyson Tuttle, G., et. al., <i>A 130 Mb/s Read/Write Channel with Digital Servo Detection</i> , (SSCC Digest of Technical Papers, pp.64-65, Feb., 1996)
	AG	Vishakhadadttta, G., et. al., <i>A 245 Mb/s EPR4 Read/Write Channel with Digital Timing Recovery</i> , (ISSCC Digest of Technical Papers, pp.388-389, Feb., 1998)
RP	AH	Darrell Dunn, <i>T1 gives up 'superchips'</i> , ELECTRONIC BUYERS' NEWS, September 25, 2000, pages 1, 124

EXAMINER RAYMOND PHAN

DATE CONSIDERED 1-31-04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.